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**INFORMATION DISCLOSURE
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<i>Complete if Known</i>	
Application Number	09/239,898
Filing Date	January 29, 1999
First Named Inventor	Seyyedy, Mirmajid
Group Art Unit	2133
Examiner Name	Lamarre, Guy <i>E</i>
Sheet 1 of 1	Attorney Docket No: 303.550US1

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>SG</i>		"16Mb Double Data Rate Synchronous Graphics RAM", <u>IBM Corporation</u> , IBM0616328RL6A,(December 1997),pp. 1-53	
<i>SG</i>		"16Mbit DDR SGRAM, 128K x 32Bit x 4 Banks Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe, Revision 0.6", <u>Samsung Electronics</u> , KM432D5131,(April 1998),pp. 1-48	

EXAMINER *SG*

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Substitute Disclosure Statement Form (PTO-1449)

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